

SIMULATOR OF THE RAIL INTERLOCKING DEVICES

Datasheet

PROVIDING SOLUTIONS FOR TOMORROW - SINCE 1993



Project objective

Create a tool for testing functional characteristics of interlocking solutions in an office environment, as well as for evaluation of the interlocking logic performance. The need for this tool was due to complex and time-consuming on-site commissioning processes. These processes can be simplified if executed during testing offsite.





Result

Simulator is used for the simulation and testing of the application logic for the wayside controller-based signal interlocking. The Simulator operates with applications for real wayside interlocking controllers that allows for imitating the behavior of every field device within various signaling scenarios without access to physical equipment. It resulted in acceleration of commissioning activities while signaling failure have been reduced after commissioning.

Scope of work

- Oevelop the tool imitating interlocking wayside controllers' behavior within various signaling scenarios
- Provide its operation with applications for real wayside interlocking controllers that execute interlocking operations on rail locations
- Provide visual control of the simulation process through communication with the Control Client
- Add support of multiple field devices (gates, switches, platform screen doors, auxiliary control evaluators, trip stops, signals, track circuits, AF902) with constant additions per request
- Include support for different customer-specific proprietary and open-source communication links (PEER, SafeP, Genisys Master, MicroLok Master, Modbus, DigiSafe) with constant updates per request
- Add support for an option to configure graphical representations for displaying relays, that is to set pictures for each of the relay types
- Implement filtering feature to display the status of each field device and bit individually to provide selective observation for each group of data
- Oisplay the log messages
- Provide simultaneous view of the visualized schemas and bits tables for the user's convenience

Activities

- Writing technical documentation
- Product testing
- 🚸 User manuals and video tutorials creation
- Support activities

- Requirements definition
- Architecture design
- Software development
- 🚸 UI development





About the project

Technologies

- C/C++
- 🍄 Qt
- 🌼 XML
- 🍄 PowerShell
- 🌻 RegExp
- 🍄 JavaScript

- 🍫 VB
- 🌻 GitLab
- 😫 SQLite
- 🕏 Python
- Inno Setup tool and scripts



Project size

- 🚸 1 Technical Coordinator
- § 5 Software Engineers
- 2 QA Engineers
- I Project Manager
- I Graphic Designer
- 🚸 1 Technical Writer

Platform

Windows

Duration



67+ months from May 2017

